Amendments to the Claims:

Without prejudice, this listing of the claims replaces all prior versions and listings of the claims in the present application:

LISTING OF CLAIMS:

1-9. (Canceled).

10. (Previously Presented) A method for selecting a function to be implemented at a terminal of a control unit, the method comprising the steps of:

selecting the function to be implemented from one of at least two different functions by using a predefinable code for providing an implemented function; and

suppressing other ones of the at least two different functions, wherein the predefinable code includes information pertaining to a waiting time corresponding to a period of time after which a switch is made from the function to be implemented to a further function of the at least two different functions, the further function being suppressed during the period of time.

11. (Previously Presented) The method according to claim 10, wherein the at least two functions are implemented by at least two different signals applicable to the terminal of the control unit, one signal of the at least two different signals of the function to be implemented is selected by the predefinable code and applied to the terminal, and other ones of the at least two different signals are suppressed.

12. (Currently Amended) The method according to claim 10, wherein one function of the at least two functions is implemented by a signal applicable to the terminal of the control unit, the signal of the function to be implemented is selected by the ~~predefineable~~ predefinable code and applied to the terminal, and other ones of the at least two different functions are suppressed.

13. (Previously Presented) The method according to claim 10, wherein one of the at least two functions includes a use of the terminal of the control unit for providing a bidirectional communication connection of another control unit to the control unit.

14. (Canceled).

15. (Previously Presented) A device for selecting a function to be implemented at a terminal, the device comprising:

a processor;

a first function path for implementing a first function; and

at least another function path for implementing at least another function;

wherein the first function path and the at least another function path run over at least two other different terminals of the processor, the first function path and the at least another function path being connected and being routed directly to the terminal, wherein:

the first function is selected for implementation by using a predefinable code for providing an implemented function, and

the predefinable code includes information pertaining to a waiting time corresponding to a period of time after which a switch is made from the first function to be implemented to the other function, the other function being suppressed during the period of time.

16. (Previously Presented) The device according to claim 15, further comprising an arrangement for selecting and clearing a function path of a function to be implemented independently of the predefinable code and for suppressing the at least another function path.

17. (Previously Presented) The device according to claim 15, wherein the first function includes a signal output unidirectionally over the first function path.

18. (Previously Presented) The device according to claim 15, wherein the at least another function includes communicating bidirectionally with a computer unit over the at least another function path.

19. (New) The method according to claim 10, wherein suppressing the at least two different functions is done without resetting any hardware arrangement.

20. (New) The device according to claim 15, wherein the other function is suppressed without resetting any hardware arrangement.